

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	John T. Moore and David L. Chapek	§	Group Art Unit:	2811
		§		
		§		
Serial No.:	09/496,794	§		
		§		
Filed:	February 2, 2000	§	Examiner:	D. Owens
		§		
		§		
For:	Trench Isolation for Semiconductor Devices	§	Atty. Dkt. No.:	MICT-0005-D1-US
		§		

Attention: D. Owens  
Commissioner for Patents  
Washington DC 20231

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STATEMENT ATTESTING TO MAILING OF  
PTO CORRESPONDENCE UNDER 37 C.F.R. § 1.8(a)

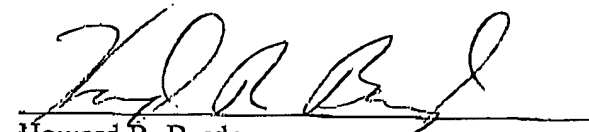
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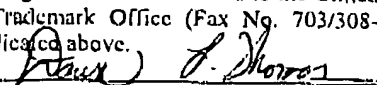
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Dear Examiner Owens:

I state that on October 17, 2001, the original of the attached copy of Reply Final Office Action Mailed October 17, 2001 and Certificate of Mailing was mailed by first class mail, with sufficient postage, in an envelope addressed to the "Commissioner for Patents, Washington DC 20231."

Date: January 14, 2002

  
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To:	Examiner D. Owens	From:	Howard R. Boyles
Company:	U.S. Patent Office Group Art Unit 3738	Date:	January 14, 2002
Fax:	703-308-7722	Pages:	6 (including coversheet)
Your Re:	SN 09/496,794	Our Re:	MICT-0005-D1-US

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Attorney Docket No.: MICT-0005-D1-US (97-0608)

Date: January 14, 2002

Applicant(s): JOHN T. MOORE AND DAVID L. CHAPEK

Serial No.: 09/496,794

Filing Date: February 2, 2000

Title: TRENCH ISOLATION FOR SEMICONDUCTOR DEVICES

1. 37 C.F.R. § 1.8(a) Statement Attesting to Mailing of PTO Correspondence
2. Reply to Final Office Action Mailed October 17, 2001

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